Pipeline stages

STAGE 0	STAGE 1	STAGE 2	STAGE 3	STAGE 4	STAGE 5
INSTR. ADDRESS CHECK	16 TO 32 BIT EXTENSION	ALU EXECUTION STEP 1	ALU EXECUTION STEP 2	ALU EXECUTION STEP 3	WRITE BACK
INSTR. FETCH	IMMEDIATE OPERAND EXTENSION	DATA ADDRESS CALCULATION	COPROCESSOR ACCESS	DATA MEMORY ACCESS	
PC INCREMENT	BRANCH ADDRESS CALCULATION	EVALUATION OF FLAGS Z, N AND C	DATA ADDRESS CHECKS	CCB ACCESS	
	REGISTER OPERAND FETCH AND FORWARD	MEMORY OPERAND FORWARD	DATA FORWARD FOR MEMORY ACCESS	PCB ACCESS	
	EXECUTION CONDITION CHECK		CONDITION REGISTER WRITE		
	INSTRUCTION CHECK				
	STATUS FLAG EVALUATION				