

instruction	The ranges of immediate constants			Note
	16 bit	32 bit		
		conditional	unconditional	
addi	$-2^6 \dots 2^6 - 1$	$-2^8 \dots 2^8 - 1$	$-2^{14} \dots 2^{14} - 1$	
addiu	$0 \dots 2^7 - 1$	$0 \dots 2^9 - 1$	$0 \dots 2^{15} - 1$	
andi	$0 \dots 2^7 - 1$	$0 \dots 2^9 - 1$	$0 \dots 2^{15} - 1$	
bxx ¹	$-2^9 \dots 2^9 - 1$	-	$-2^{21} \dots 2^{21} - 1$	
chrs	0...3	-	0..3	
cmpi	$-2^6 \dots 2^6 - 1$	-	$-2^{16} \dots 2^{16} - 1$	
cop	-	-	0...3 ²	Used only in 32 bit mode
exb	0...3	0..3	0...3	
exbfi	-	-	$0 \dots 2^{10} - 1$	Used only in 32 bit mode
exh	0 or 1	0 or 1	0 or 1	
jal	$-2^9 \dots 2^9 - 1$	-	$-2^{24} \dots 2^{24} - 1$	
jmp	$-2^9 \dots 2^9 - 1$	-	$-2^{24} \dots 2^{24} - 1$	
ld	-8...7	$-2^8 \dots 2^8 - 1$	$-2^{14} \dots 2^{14} - 1$	
lli	-	-	$0 \dots 2^{16} - 1$ (or $-2^{15} \dots 2^{15} - 1$)	Used only in 32 bit mode
lui	-	-	$0 \dots 2^{16} - 1$ (or $-2^{15} \dots 2^{15} - 1$)	Used only in 32 bit mode
movfc	0...3	0..3	0..3	
movtc	0...3	0..3	0..3	
muli	$-2^6 \dots 2^6 - 1$	$-2^8 \dots 2^8 - 1$	$-2^{14} \dots 2^{14} - 1$	
ori	$0 \dots 2^7 - 1$	$0 \dots 2^9 - 1$	$0 \dots 2^{15} - 1$	
sexti	0...31	0...31	0...31	
slli	0...32	0...32	0...32	
srai	0...32	0...32	0...32	
srli	0...32	0...32	0...32	
st	-8...7	$-2^8 \dots 2^8 - 1$	$-2^{14} \dots 2^{14} - 1$	
swm	16 or 32	-	16 or 32	

table 1, Values allowed for immediate constats

¹ xx is one of the following: c, blt, bne, bgt, beq, begt tai belt

² imm1 is a value 0...3. Imm2 is an instruction word for a coprocessor.
(See Instruction Specification.)